AM2332N

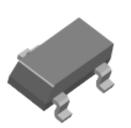
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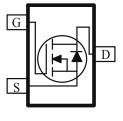
N-Channel 20V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOT-23 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(\Omega)$ $I_D(A)$		
20	$0.058 @ V_{GS} = 4.5 V$	4.7	
	0.082 @ V _{GS} = 2.5V	4.0	





ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter			Maximum	Units		
Drain-Source Voltage			20	V		
Gate-Source Voltage			±12	v		
Continuous Drain Current ^a	T _A =25°C	I.	4.7			
Continuous Drain Current	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	ID	3.8	А		
Pulsed Drain Current ^b		I _{DM}	±20			
Continuous Source Current (Diode Conduction) ^a			5 1.6			
	T _A =25°C	D_	1.3	W		
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	гD	0.8			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum Uni		
Maximum Junction-to-Ambient ^a	t <= 5 sec	D	100		
	Steady-State	R _{THJA}	166	C/W	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

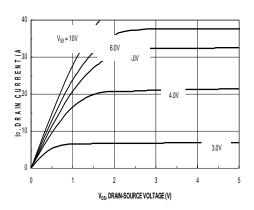
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SPECIFICATIONS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Symbol Test Conditions	Limits			Unit	
1 al ameter	Symbol	Symbol Test Conditions		Тур	Max	Unit	
Static							
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	0.7	0.8	1.5	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$		5.4	±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 16 V, V_{GS} = 0 V$		8nA	1	uA	
	1088	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			10		
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 4.5 V$	10			Α	
Drain-Source On-Resistance ^A	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 4.7 \text{ A}$		41	58	mΩ	
Dram-Source On-Resistance	¹ DS(on)	$V_{GS} = 2.5 \text{ V}, I_D = 4 \text{ A}$		56	82		
Forward Tranconductance ^A	g _{fs}	$V_{DS} = 10 \text{ V}, I_D = 4.7 \text{ A}$		11.3		S	
Diode Forward Voltage	V _{SD}	$I_{S} = 1.6 \text{ A}, V_{GS} = 0 \text{ V}$		0.75		V	
Dynamic ^b							
Total Gate Charge	Qg			7.5	20		
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 4.7 \text{ A}$		0.6	2	nC	
Gate-Drain Charge	Q _{gd}	1 1		1.0	3		
Input Capacitance	C _{iss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1MHz		720	2000	pF	
Output Capacitance	C _{oss}			165	400		
Reverse Transfer Capacitance	C _{rss}	I = I M H Z		60	200		
Turn-On Delay Time	t _{d(on)}			8	20		
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_L = 15 \Omega, I_D = 1 \text{ A},$		24	50	ma	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 4.5 V$		35	80	ns	
Fall-Time	t _f			10	30		

Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Typical Electrical Characteristics (N-Channel)

Figure 1. On-Region Characteristics

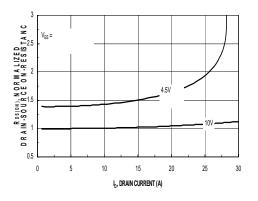


Figure 3. On Resistance Vs Vgs Voltage

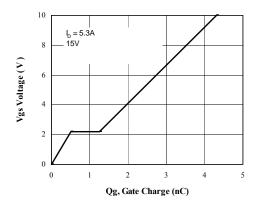


Figure 5. Gate Charge Characteristics

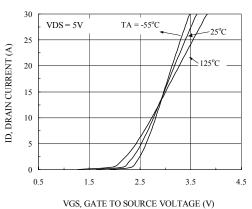


Figure 2. Body Diode Forward Voltage Variation

with Source Current and Temperature

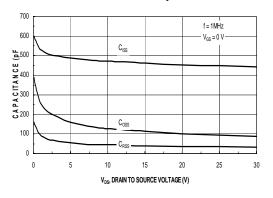


Figure 4. Capacitance Characteristics

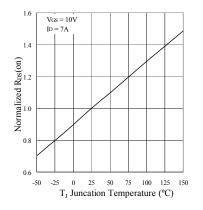
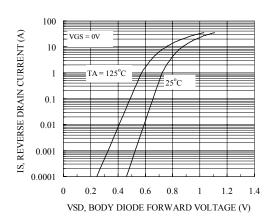
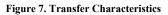


Figure 6. On-Resistance Variation with Temperature

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Typical Electrical Characteristics (N-Channel)



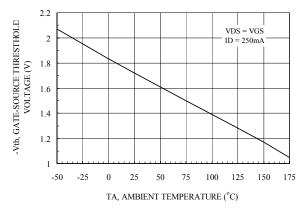


Figure 9. Vth Gate to Source Voltage Vs Temperature

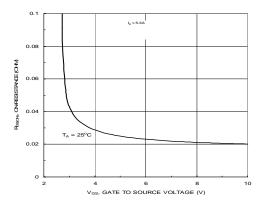


Figure 8. On-Resistance with Gate to Source Voltage

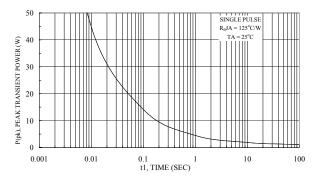
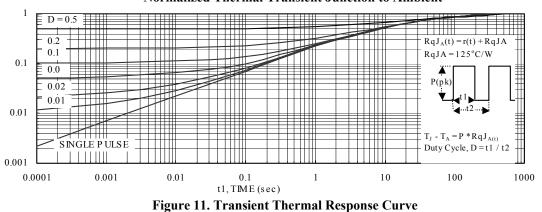


Figure 10. Single Pulse Maximum Power Dissipation



Normalized Thermal Transient Junction to Ambient

Package Information

